

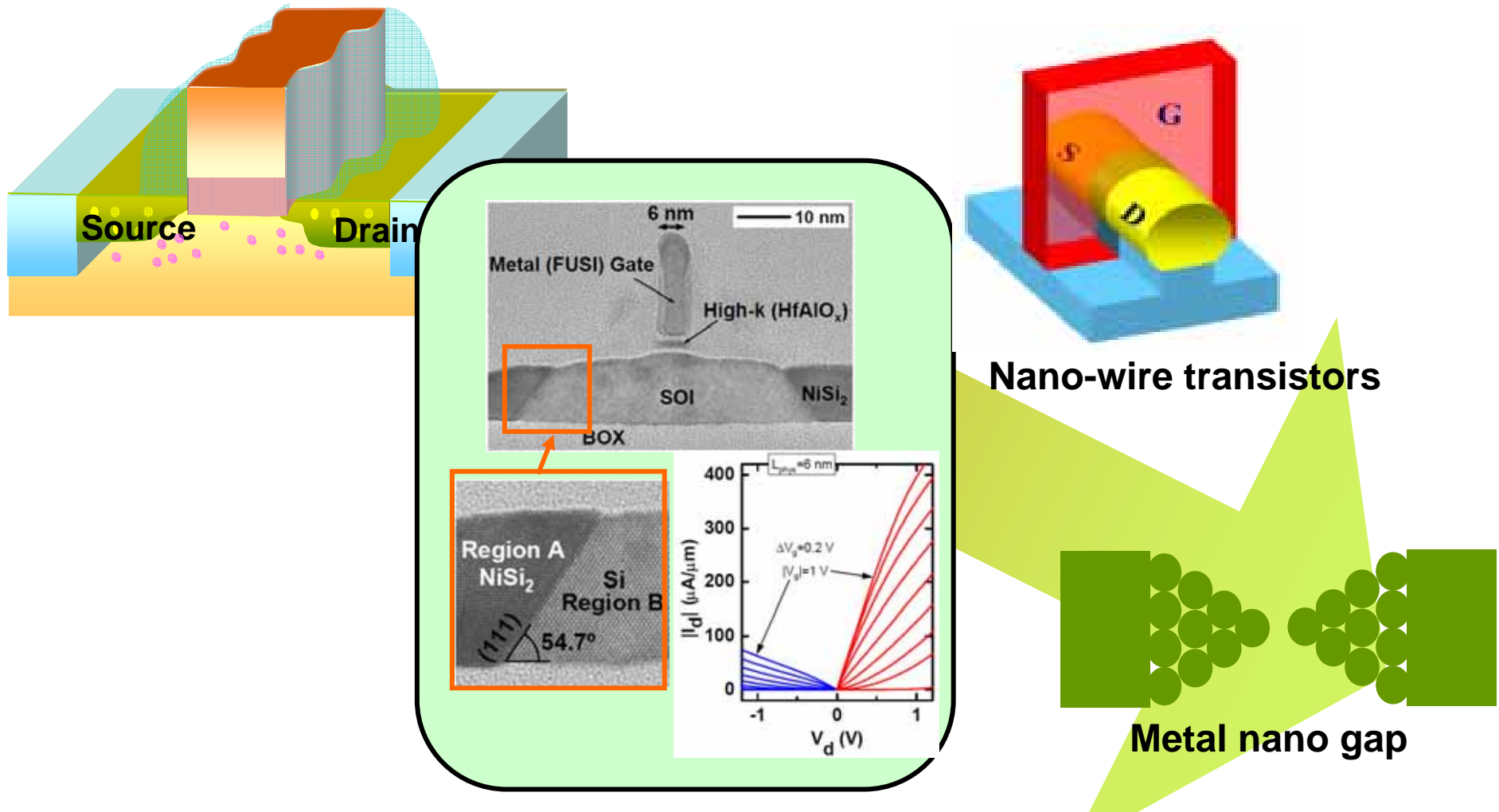
Metrology and Characterization for Nanoelectronics

Toshihiko Kanayama

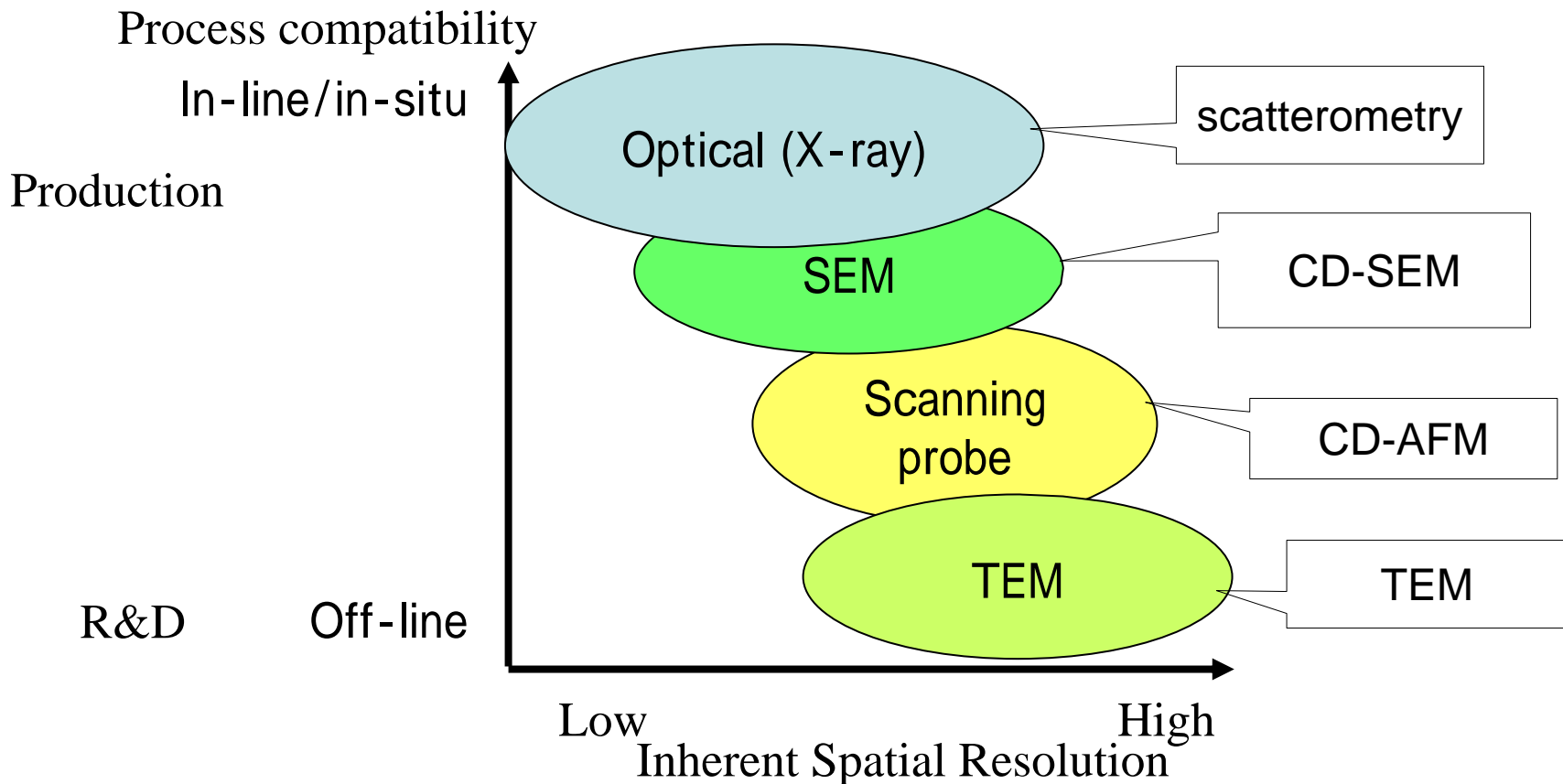
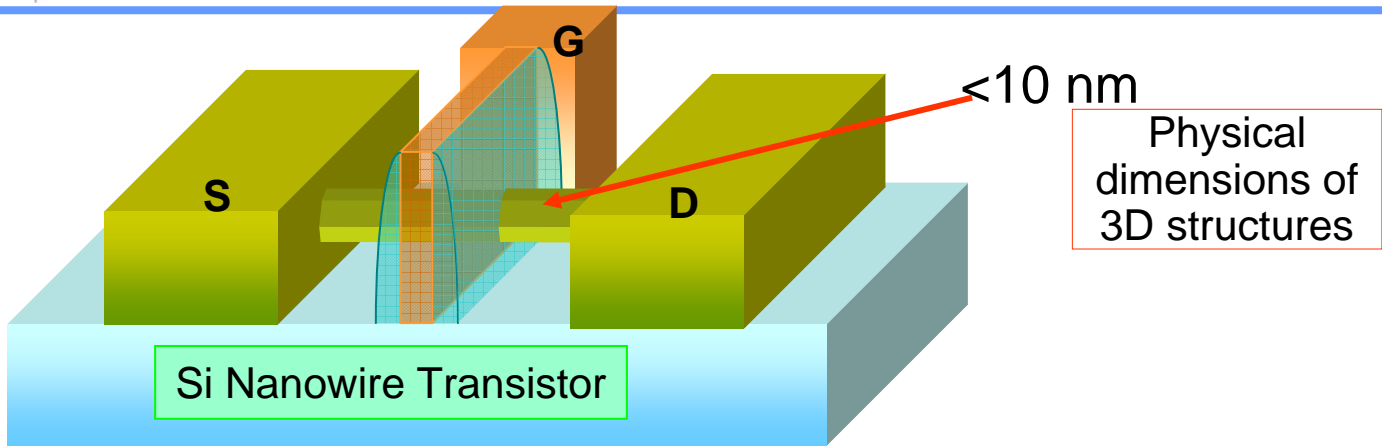
MIRAI-Advanced Semiconductor Research Center,
AIST, Japan

- Why do we need measurement/characterization techniques for nanoelectronics?
- Characterization/metrology for local probing of material structures and properties
 - Physical dimensions
 - Local strain in Si
 - Dopant / potential distributions
- Conclusion

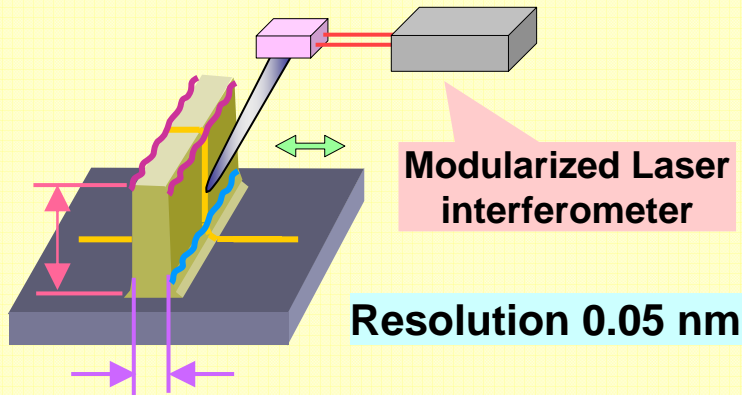
Nanoelectronics: Exploitation of Nanostructures for Information Processing
Performance is sensitive to structures.



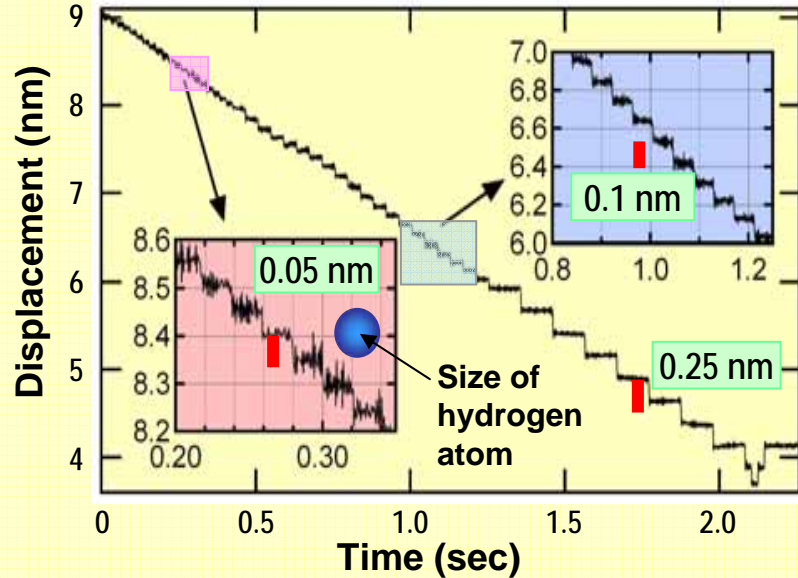
To understand and control nanoelectronics devices while minimizing variation, Characterization and metrology of local properties and structures are needed.



CD-AFM with Laser interferometer

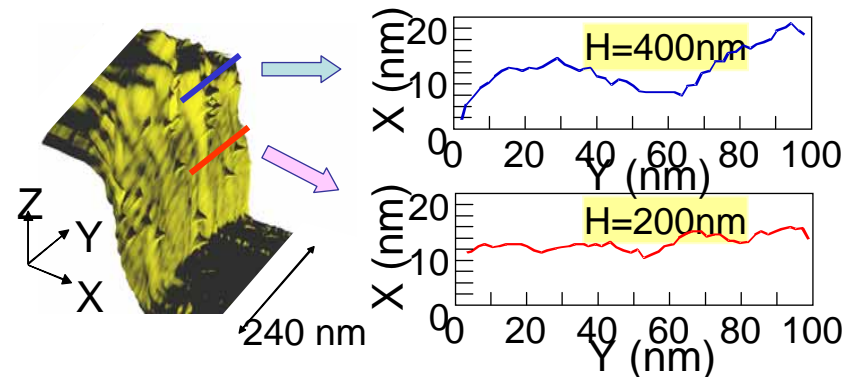
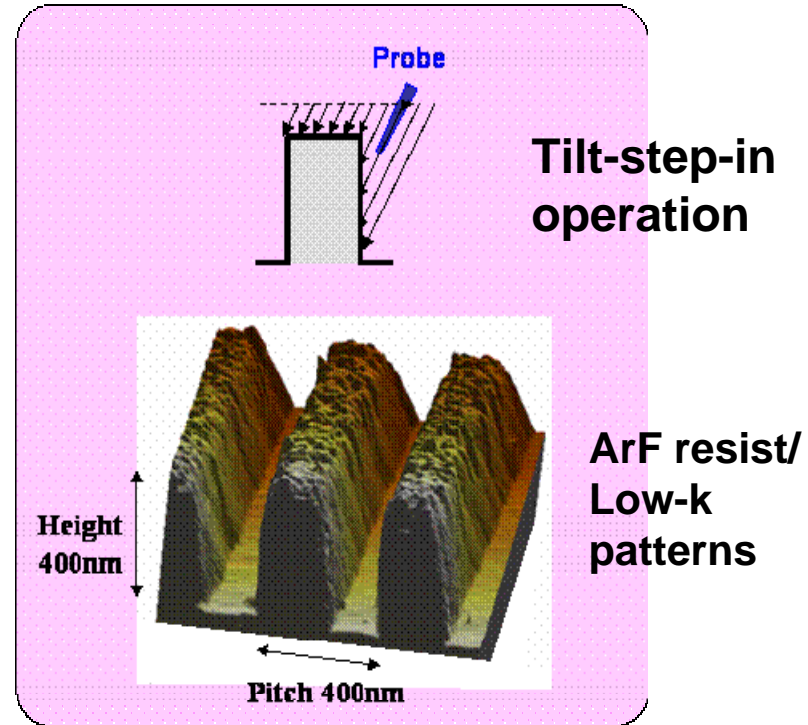


- ◆ 3D AFM scanner: parallel spring mechanism.
- ◆ Laser interferometer: DSP-based processing.



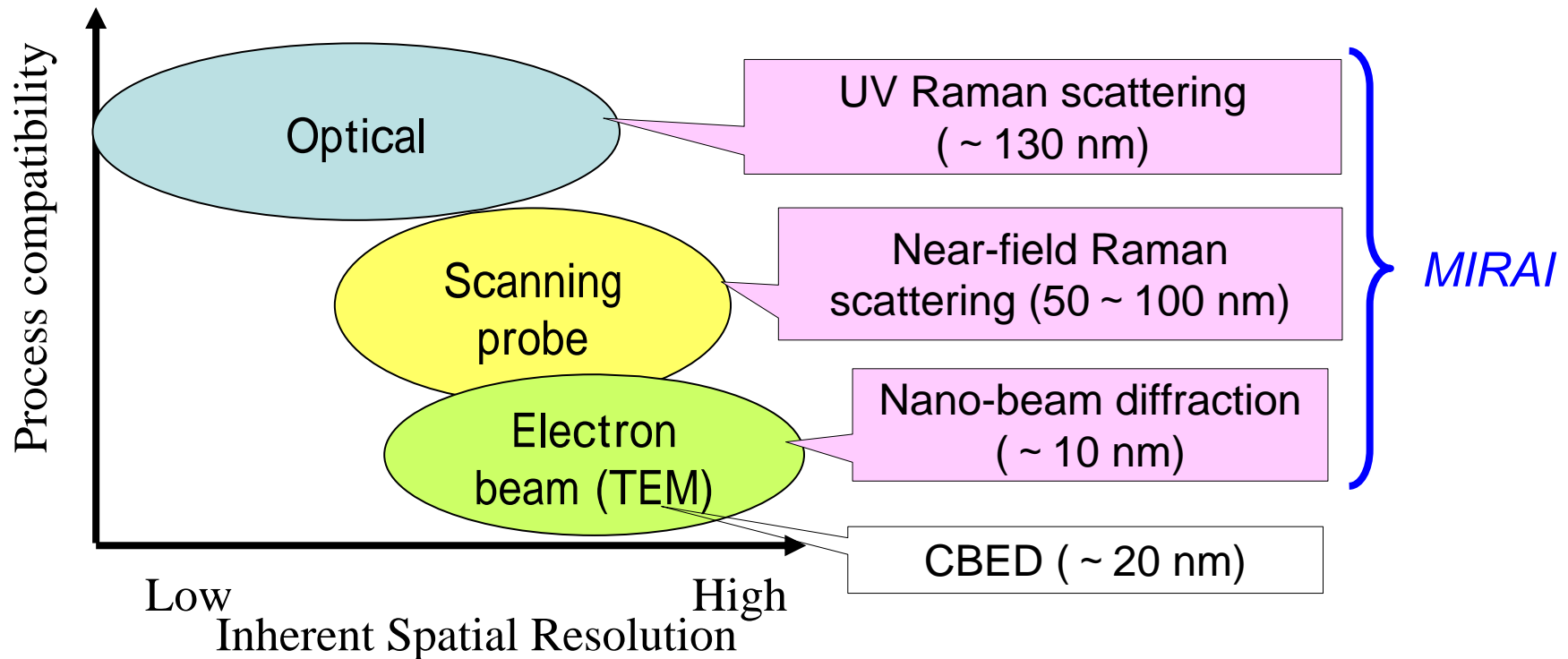
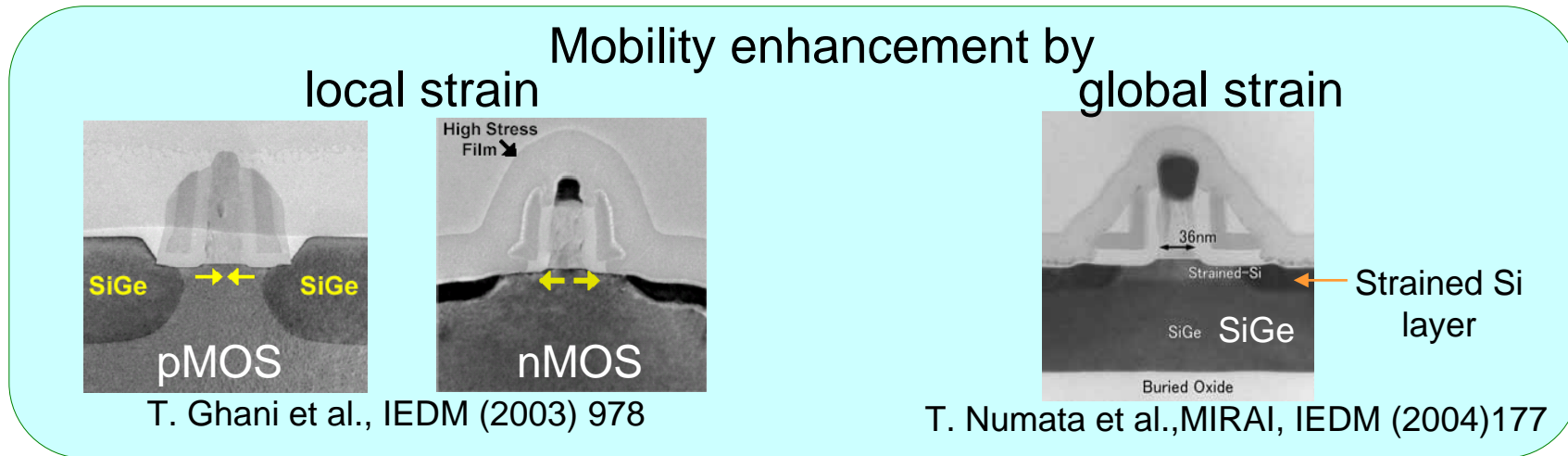
S. Gonda et al., Characterization and Metrology for ULSI Tech., 2005

Sidewall and line edge roughness measured by tilt-step-in operation

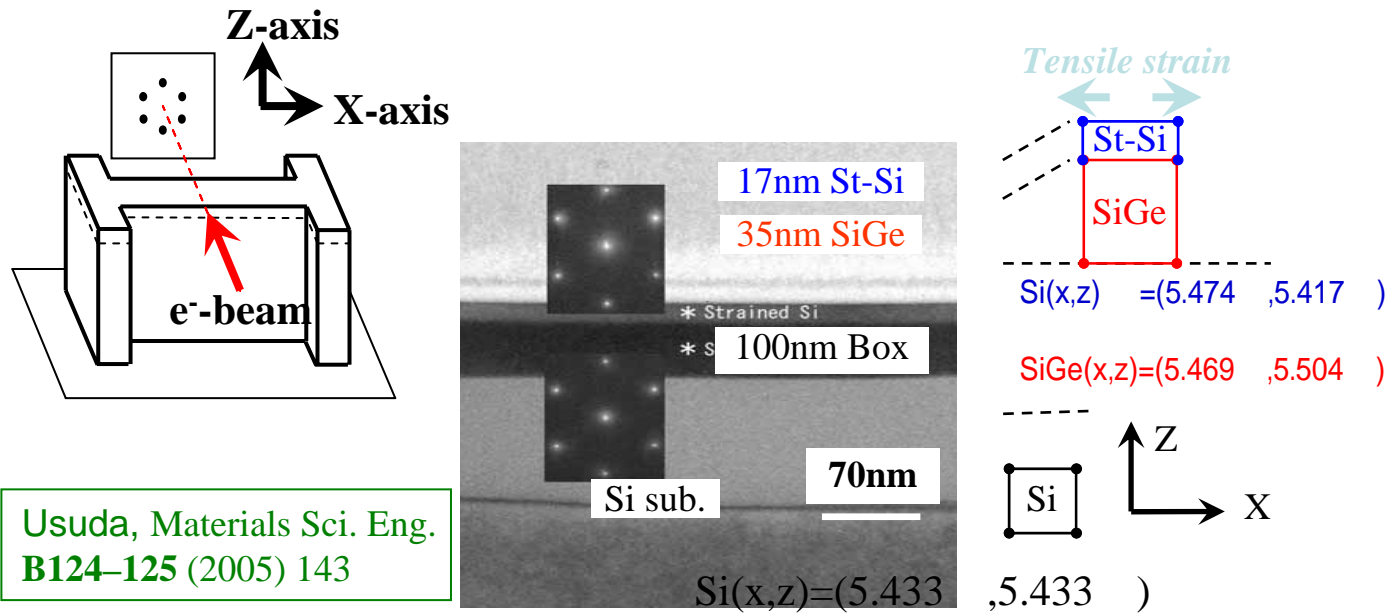


K. Murayama et al, SPIE, 2006

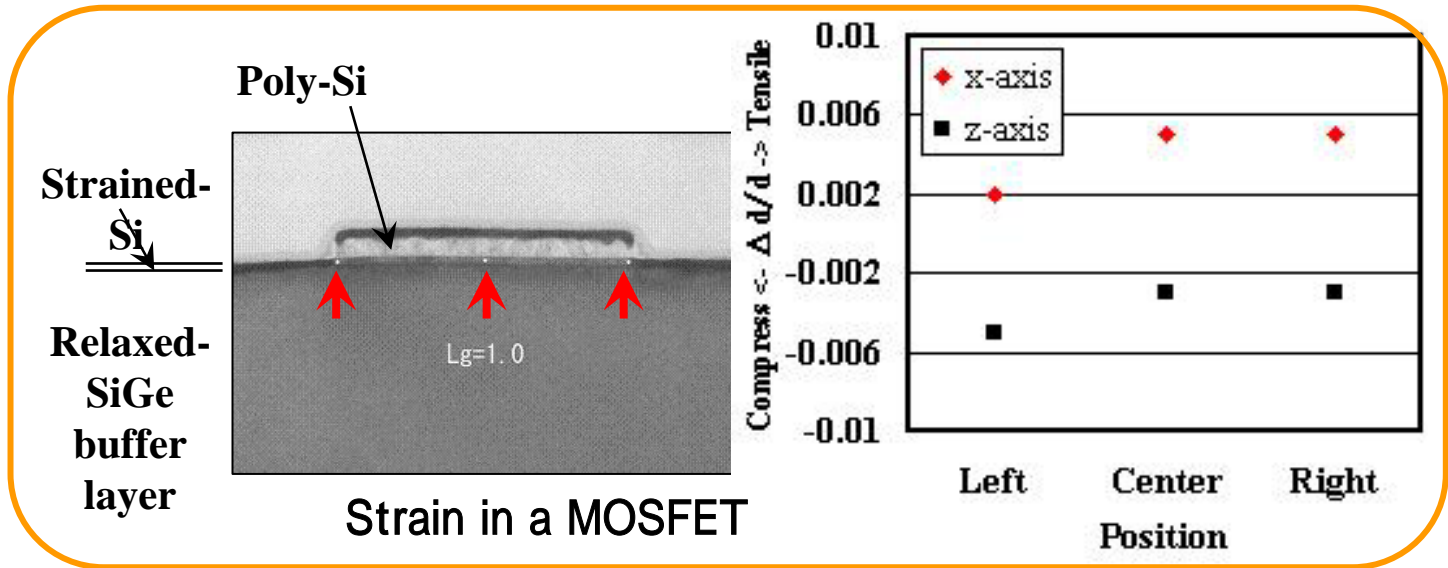
Strain distribution

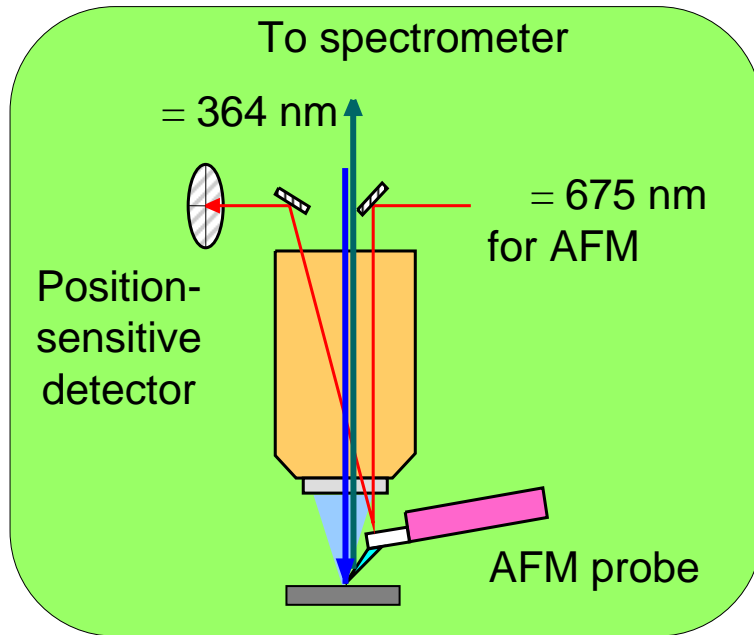


NBD (NanoBeam electron Diffraction)

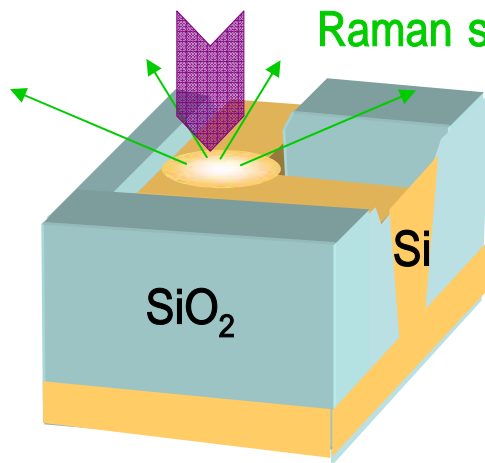


Usuda, Materials Sci. Eng. B124-125 (2005) 143

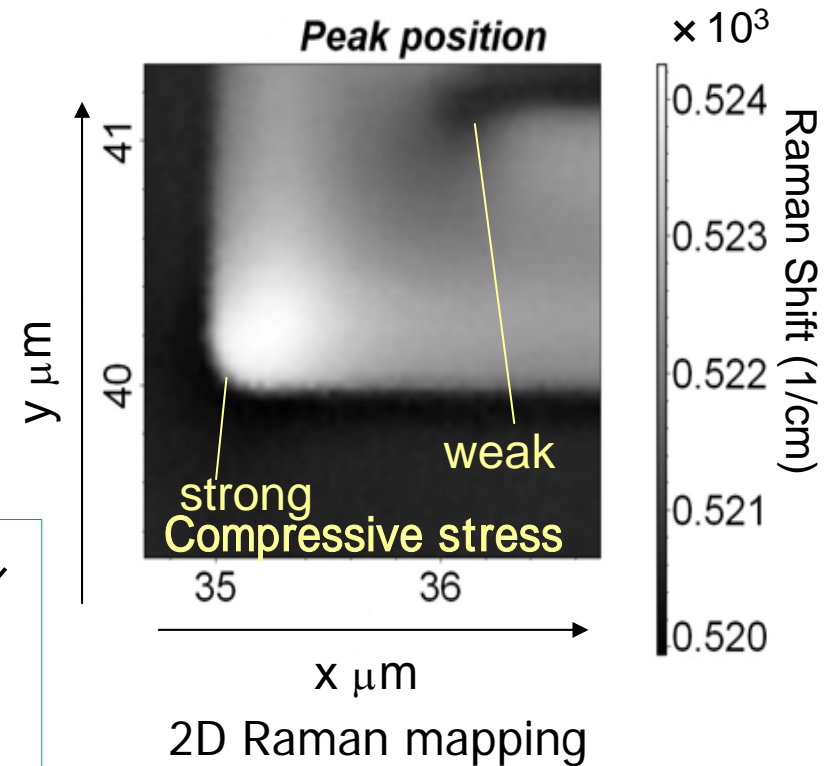
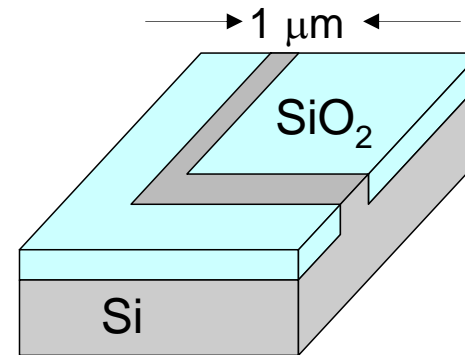




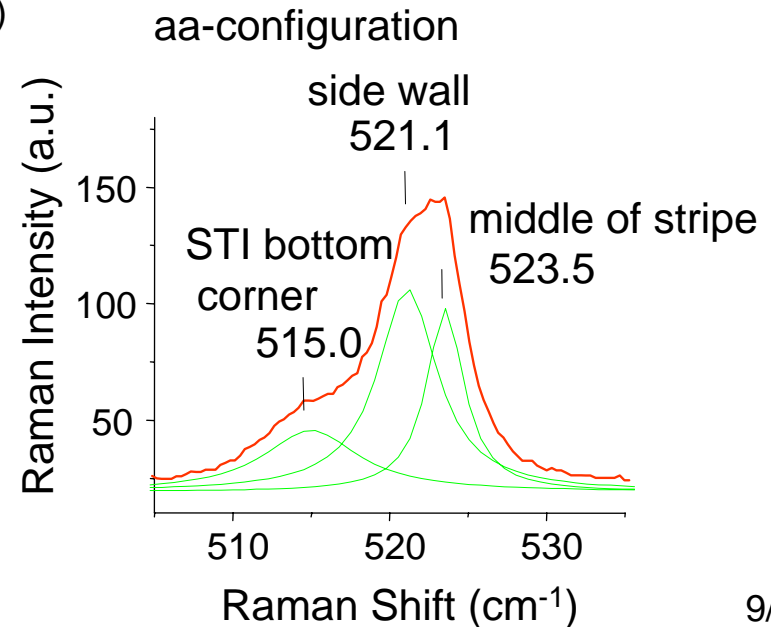
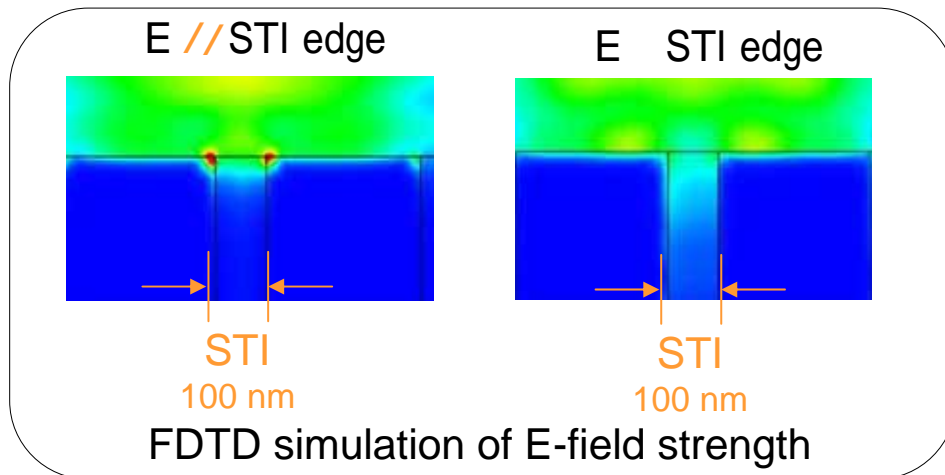
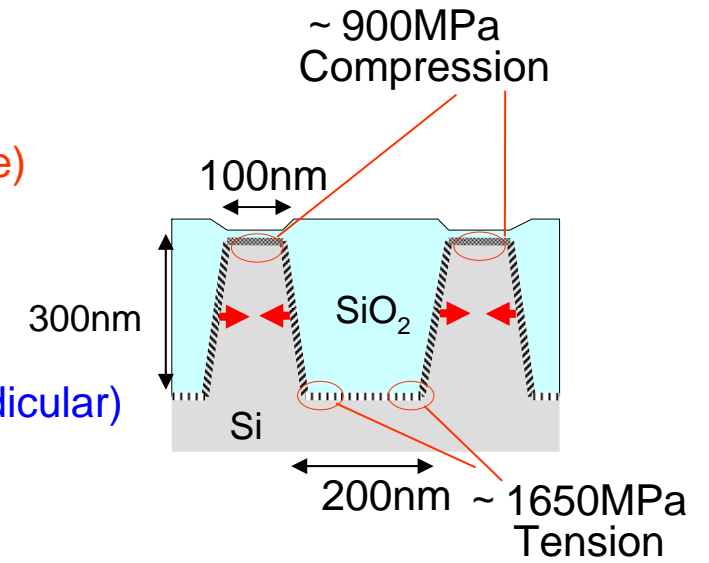
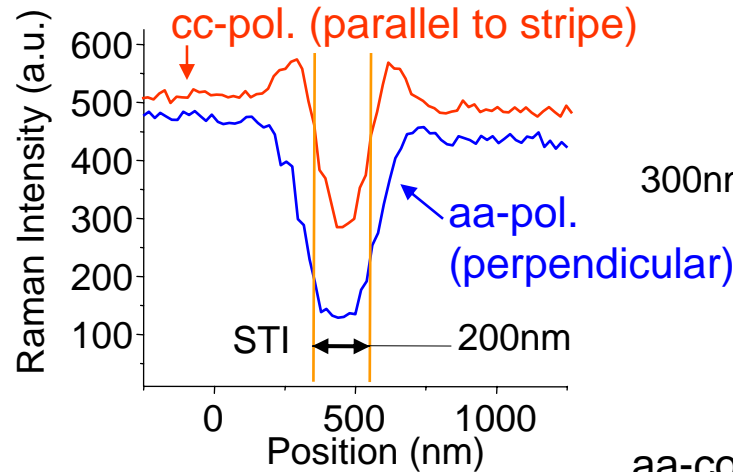
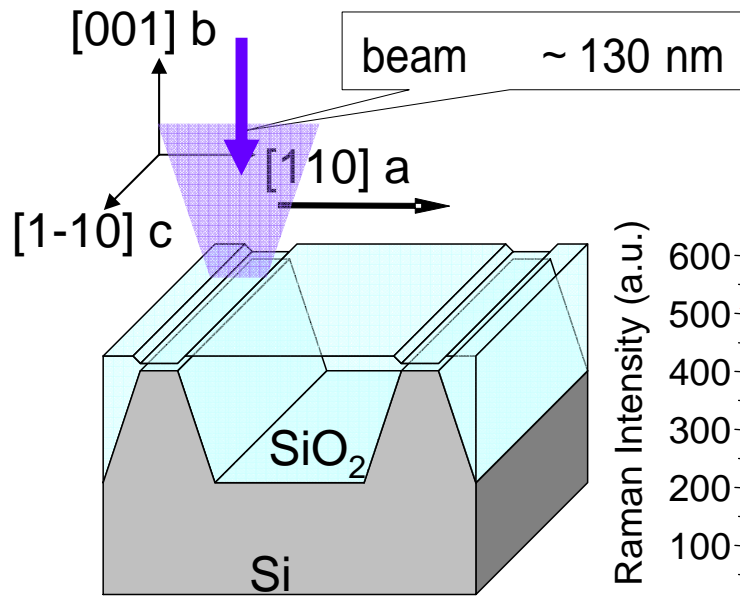
Excitation
= 364 nm, ~ 130 nm

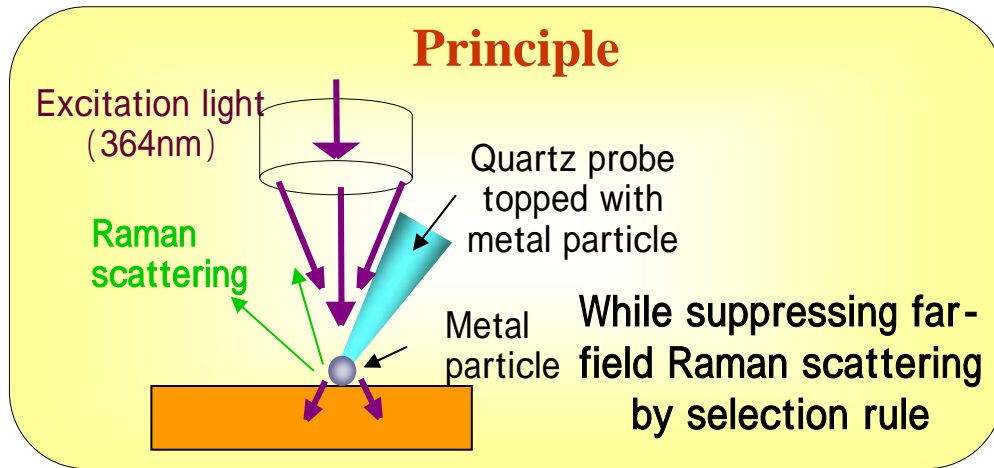


Raman shift:
 $1 / \lambda_{out} - 1 / \lambda_{in}$
 = phonon vibration
 For strain in Si
 $(1 / \lambda) = 723 \text{ cm}^{-1}$

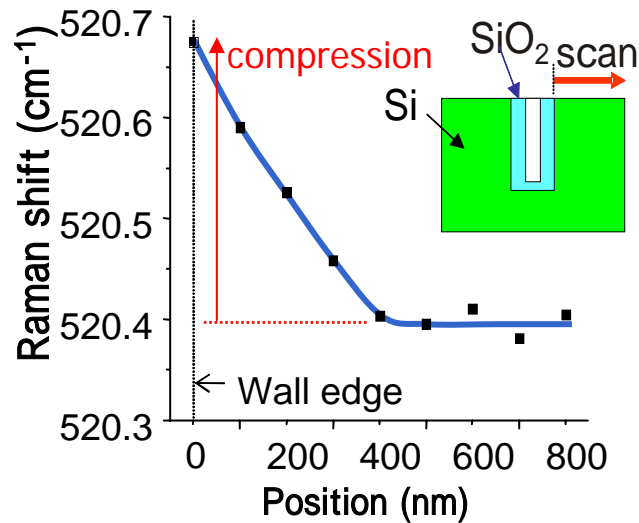
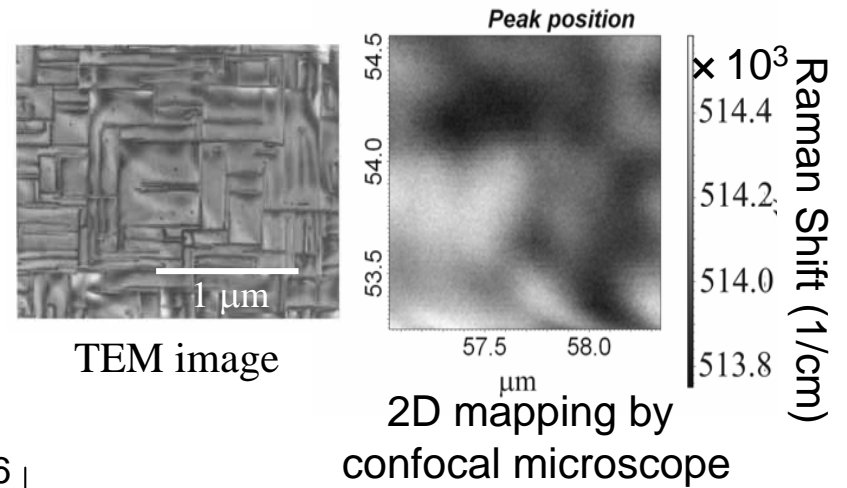


Poborchii, Appl. Phys. Lett. **89** (2006) 233505

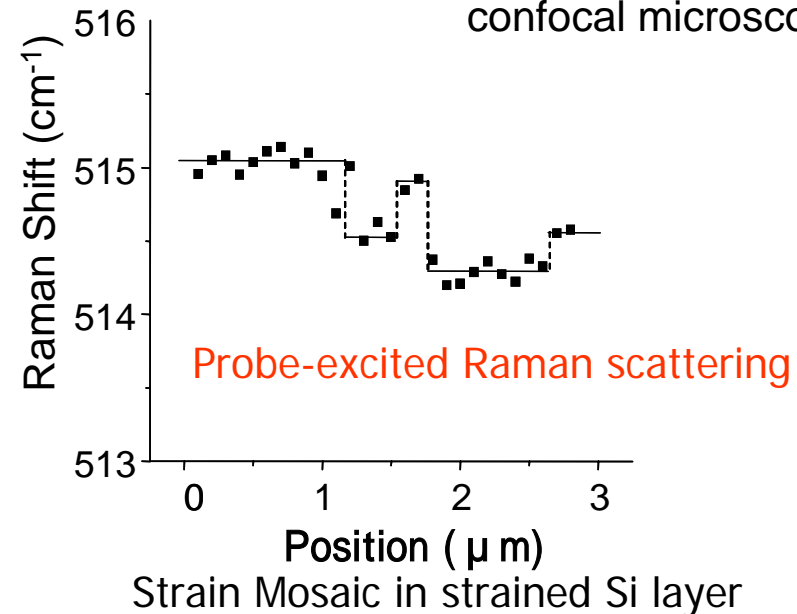




25nm-thick Strained Si on 27nm-thick Si_{0.7}Ge_{0.3}



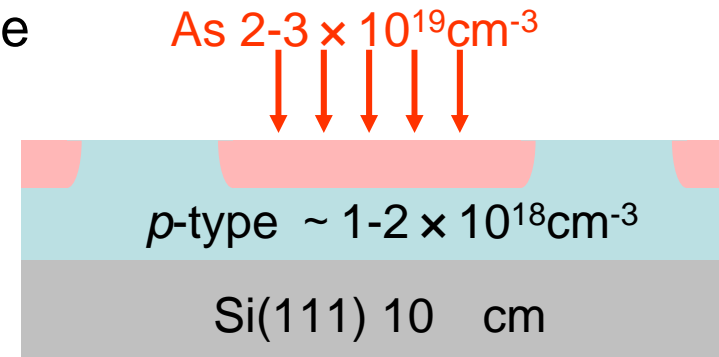
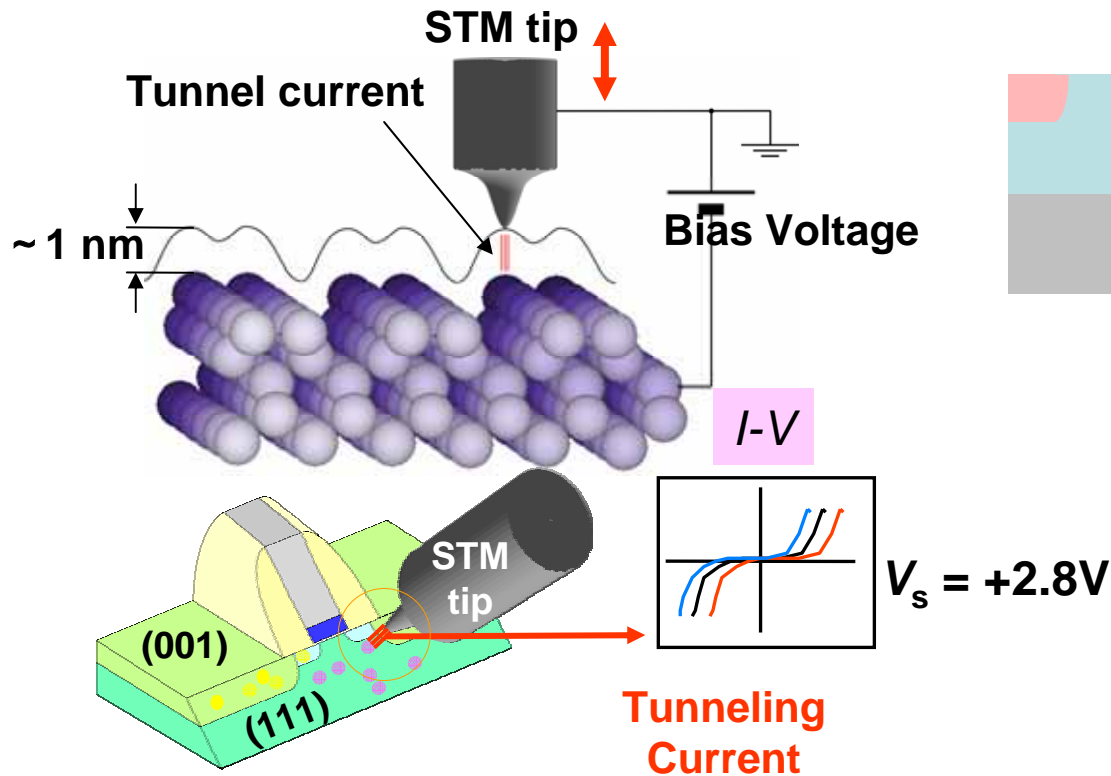
Stress distribution around a oxidized hole



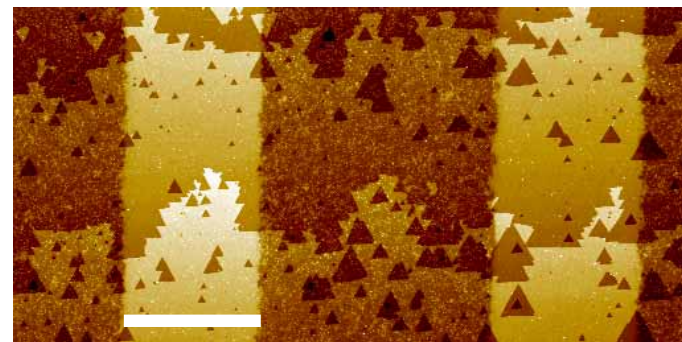
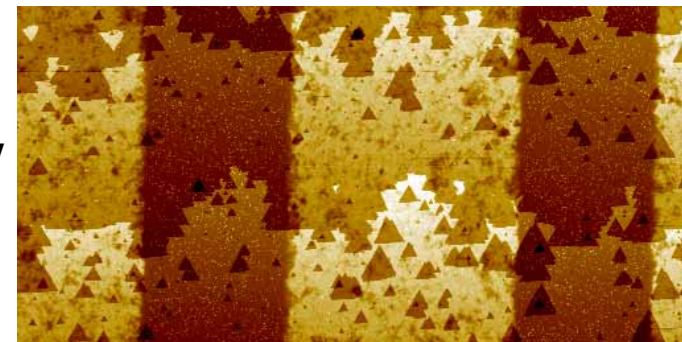
STM for potential and dopant profiling

Scanning Tunneling Microscopy (STM)

STM Topography : height image



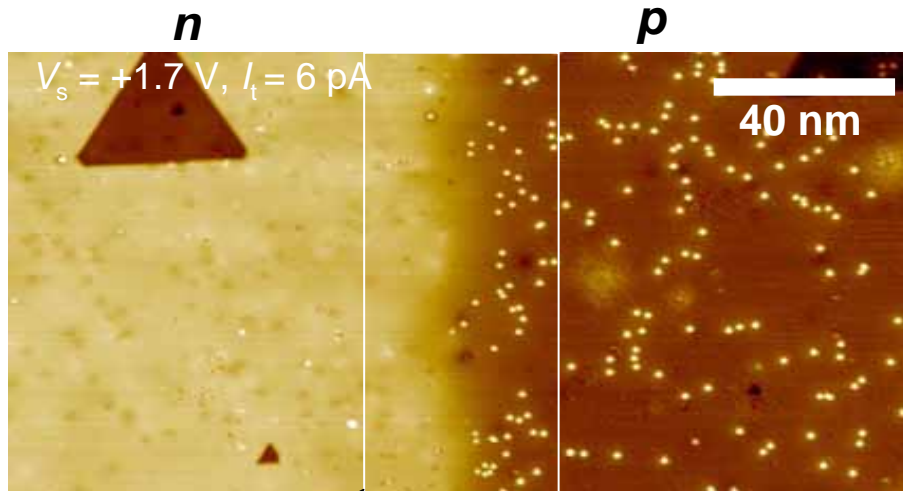
n p n p



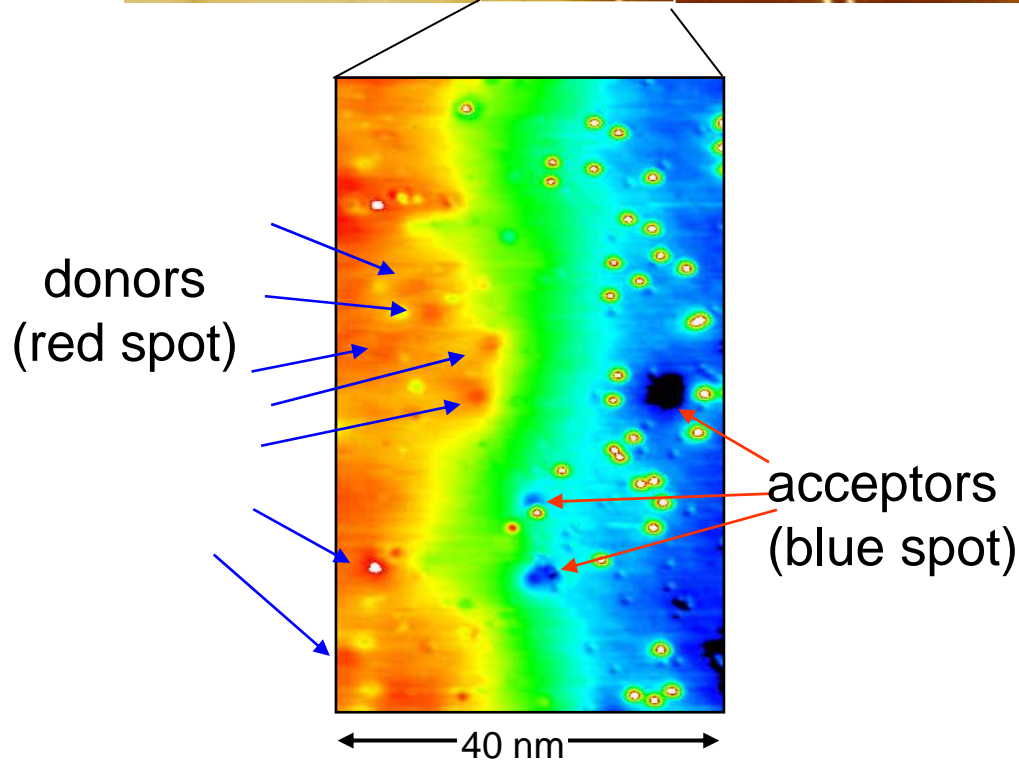
400 nm

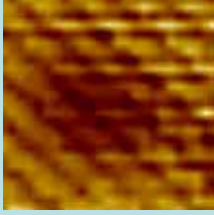
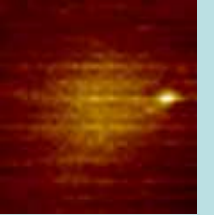
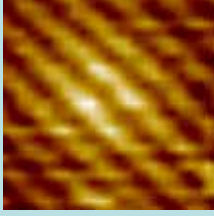
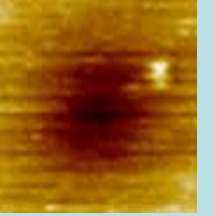
Key process
 Flattening and hydrogenation of (111) surface by aqueous NH_4F treatment followed by dopant reactivation at $\sim 400^\circ\text{C}$

Simultaneous measurement of potential and dopant atom

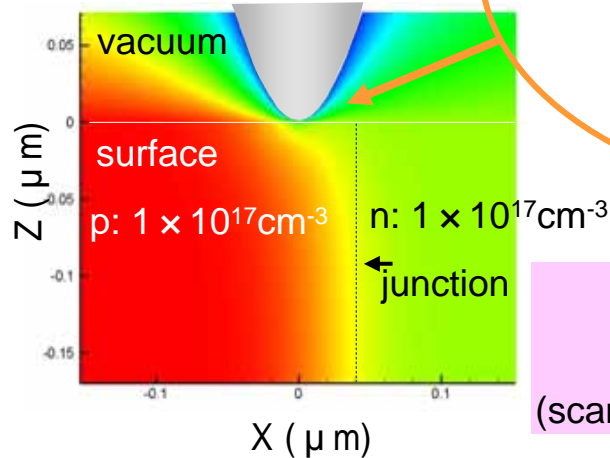


Donor distribution correlates with the potential fluctuation.

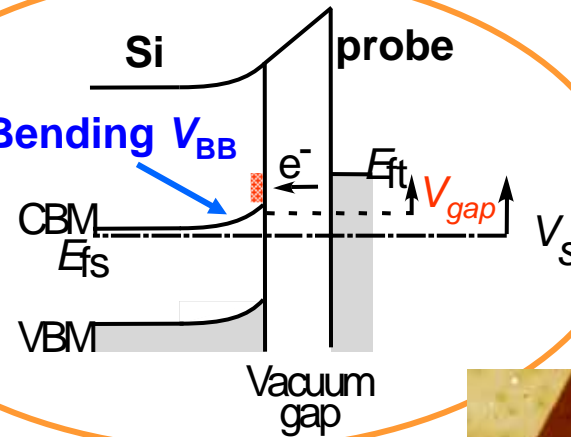


Imaging of dopant atoms		
V_s	acceptor negative charge	donor positive charge
Substrate bias voltage: $V_s > 0$		
$V_s < 0$		

Potential simulation



Band Bending V_{BB}



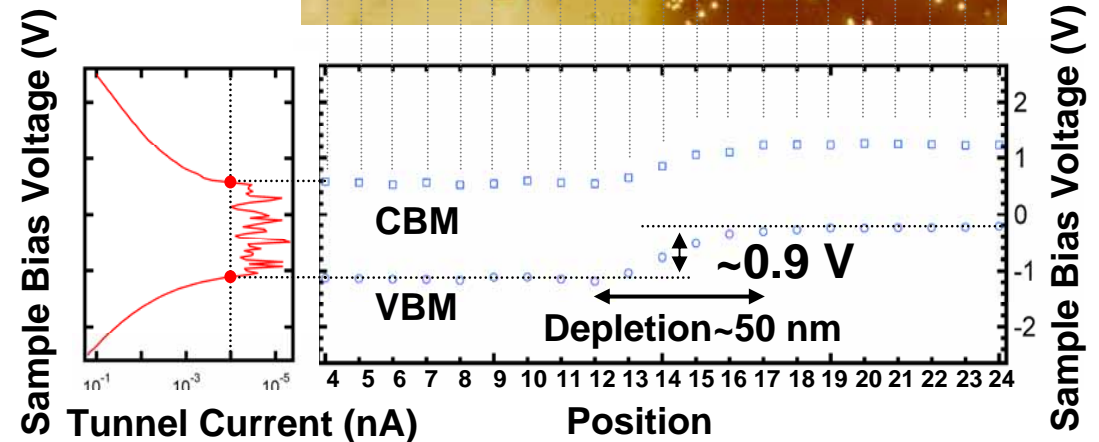
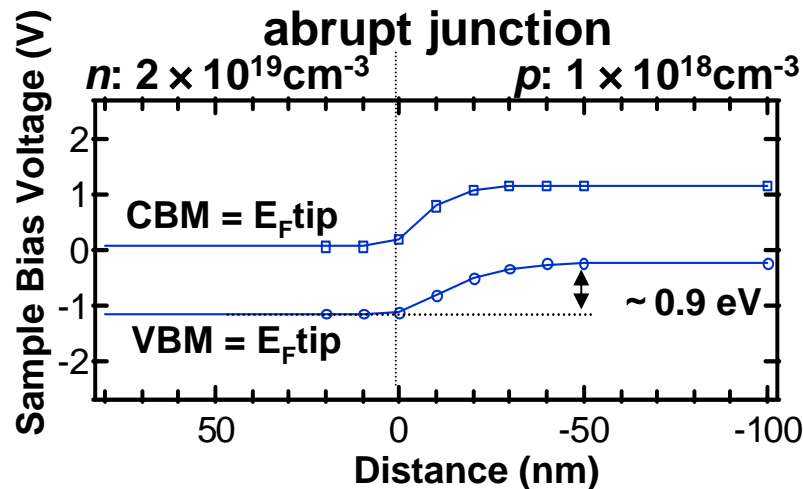
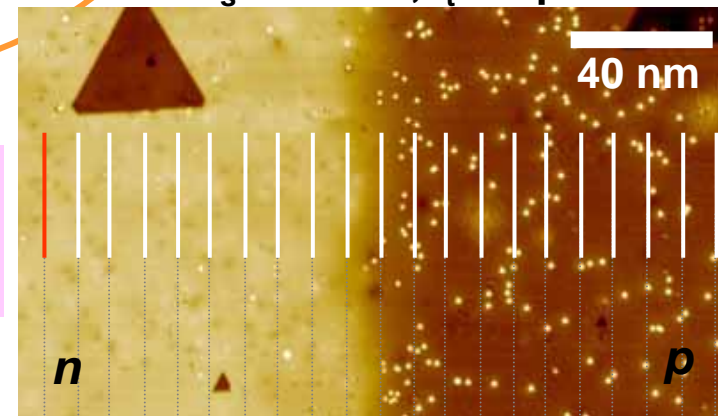
Issues

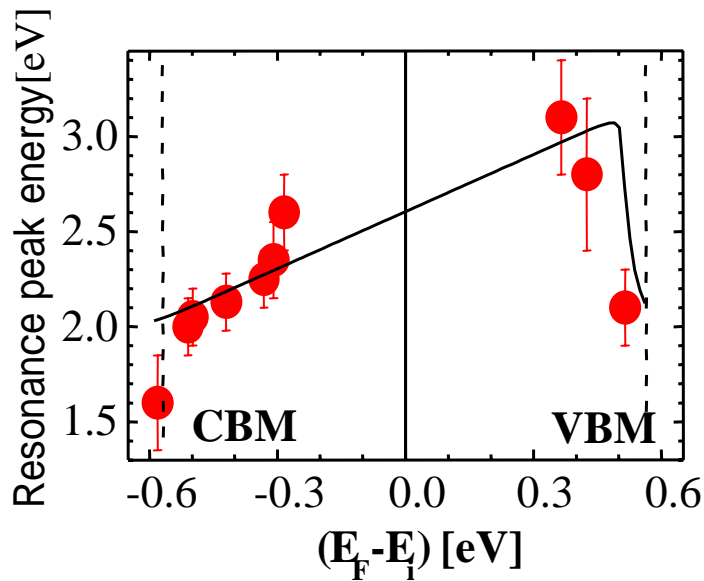
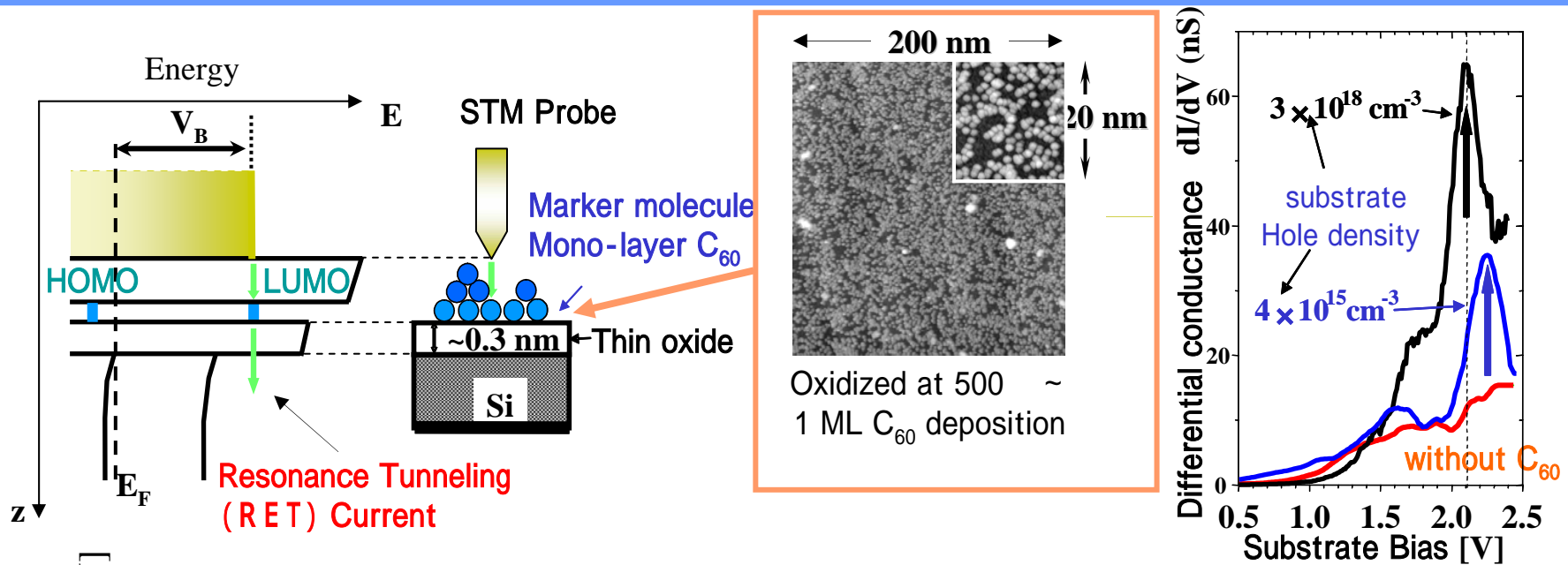
- Potential distortion by STM bias
- $V_s = V_{gap} + V_{BB}$
 \swarrow
 unknown

$V_s = +1.7 \text{ V}, I_t = 6 \text{ pA}$

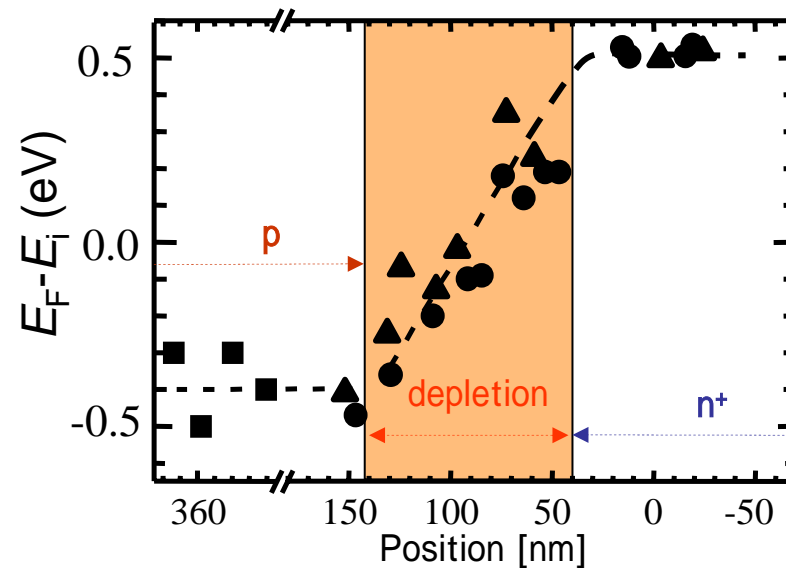
I - V measurement
STS

(scanning tunneling spectroscopy)





Resonance energy vs. Substrate Fermi level

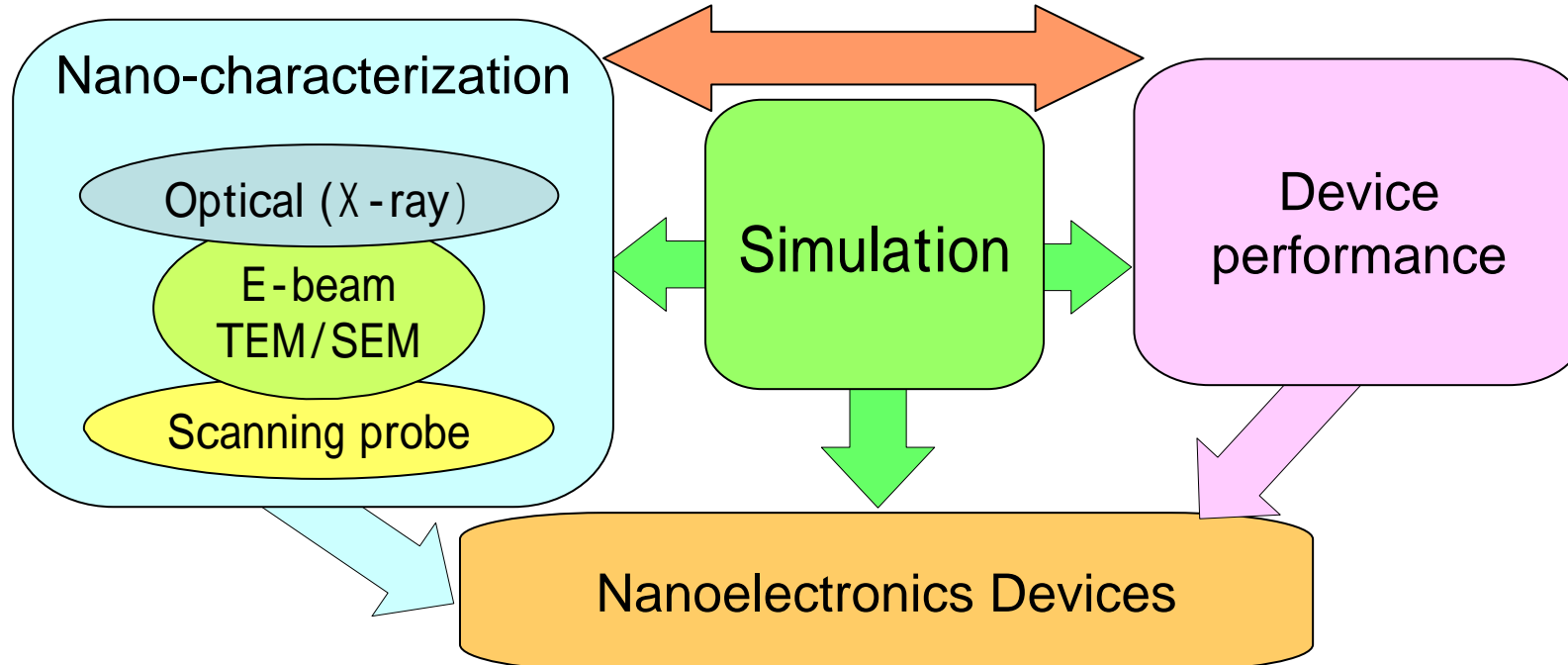


Potential profile in a pn junction region

Conclusion

For R&D of Nanoelectronics Devices

- Characterization and metrology of local properties and structures are needed.
 - e.g., CD & LER, Local strain in Si, Potential distributions
- Various methods must be comprehensively used;
 - Optical, SEM/TEM, Scanning probein conjunction with Simulations, because no single method can give complete information in nm regions.



Colleagues in MIRAI project

T. Tada and V.V. Porochii for UV Raman measurements

L. Bolotov and M. Nishizawa for STM measurements

K. Usuda for Nano-beam diffraction

S. Gonda for CD-AFM

N. Hirashita, T. Numata, T. Tezuka, N. Sugiyama and S. Takagi and many other members of the MIRAI project for providing STI and strained SOI structures

Sample preparation

N. Hattori of Renesas Technology for strained STI structures

H. Fukutome of Fujitsu Laboratory Ltd. for the p - n junction samples.

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