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# **Present and Future Opportunities for EU-Japan Collaboration in Semiconductor Manufacturing**

## **Roadmaps, Guidelines and Technology Assessment**

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**Part 1: Toshihiko Osada**

**Part 2: Lothar Pfitzner**

2008 EU-Japan Cooperation Forum on ICT Research  
March 4-5, 2008  
Mita Kaigisho Conference Hall, Tokyo, Japan

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# JEITA300PrimeGuideLine

<http://www.jeita-smtj.com/>

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# Semiconductor Manufacturing – The Role of Equipment Development, Assessment and Control

## Areas for Effective International Cooperation

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